

# SiC Schottky Barrier Diode

#### **SCS110KE2**

#### Applications

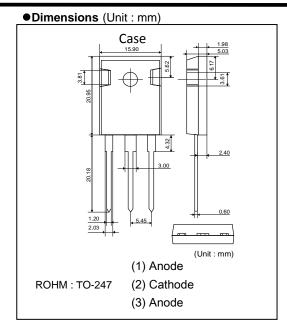
General rectification

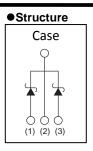
#### Features

- 1)Shorter recovery time
- 2)Reduced temperature dependence
- 3)High-speed switching possible

#### Construction

Silicon carbide epitaxial planer type





● Absolute maximum ratings (Tj=25°C)

Parameter	Symbol	Limits	Unit V	
Reverse voltage (repetitive peak)	$V_{RM}$	1200		
Reverse voltage (DC)	$V_R$	1200	V	
Continuous forward forward current *6	I <sub>F</sub>	5 / 10 * <sup>1</sup>	А	
Course as a secretitive femoral course to	1	24 / 48 *2	А	
Surge no repetitive forward current *6	I <sub>FSM</sub>	97 / 194* <sup>3</sup>	Α	
Repetitive peak forward current &	I <sub>FRM</sub>	20 / 39*4	А	
Total power disspation *6	$P_{D}$	83 / 160* <sup>5</sup>	W	
Junction temperature	Tj	175	°C	
Range of storage temperature	Tstg	−55 to +175	°C	
Junction to case *6	Rth(j-c)	1.8 / 0.93	°C / W	

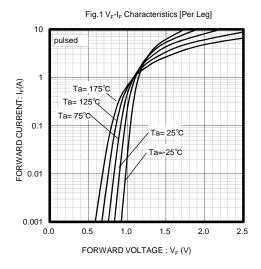
<sup>(\*1)</sup>Tc=153°C / 151°C (\*2)PW=8.3ms sinusoidal,Tj=25°C

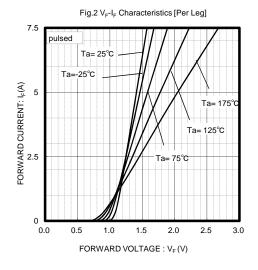
#### ●Electrical characteristics (Tj=25°C) [Per Leg]

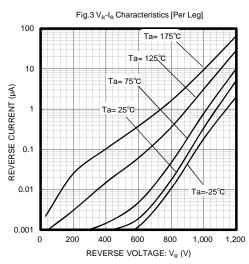
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
DC blocking voltage	$V_{DC}$	1200	-	-	V	I <sub>R</sub> =0.1mA
Forward voltage	$V_{F}$	-	1.50	1.75	V	I <sub>F</sub> =5A,Tj=25°C
	٧F	-	2.00	-	V	I <sub>F</sub> =5A,Tj=175°C
Reverse current	L	-	5	100	μΑ	V <sub>R</sub> =1200V,Tj=25°C
	I <sub>R</sub>	-	60	-	μΑ	V <sub>R</sub> =1200V,Tj=175°C
Total capacitance	С	-	325	-	pF	V <sub>R</sub> =1V,f=1MHz
		-	25	-	pF	V <sub>R</sub> =800V,f=1MHz
Total capacitive charge	Qc	-	20	-	nC	V <sub>R</sub> =800V,di/dt=500A/µs
Switching time	tc	-	15	-	ns	V <sub>R</sub> =800V,di/dt=500A/μs

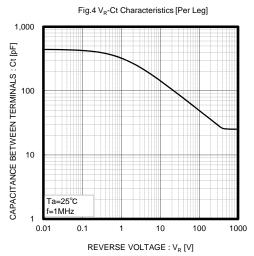
<sup>(\*3)</sup>PW=10 μs square,Tj=25°C (\*4)Tc=120°C,Tj=150°C,Duty cycle=10% (\*5)Tc=25°C (\*6)Per Leg / Per Device

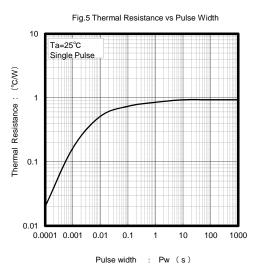
#### ●Electrical characteristic curves (Ta=25°C)

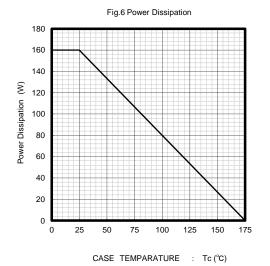


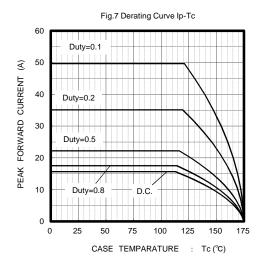


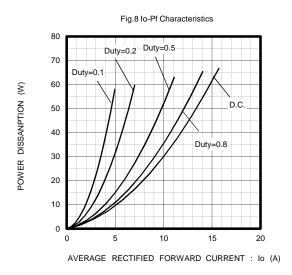












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