

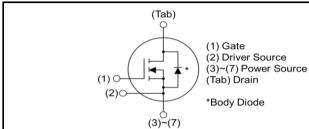
# SCT3060AW7 **N-channel SiC power MOSFET**

159W

### Outline



## Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

## Packaging specifications

	Packing	Embossed tape
	Reel size (mm)	330
Type	Tape width (mm)	24
Туре	Basic ordering unit (pcs)	1000
	Taping code	TL
	Marking	SCT3060AW7

## Features 1) Low on-resistance

2) Fast switching speed

 $\overline{\mathsf{P}}_{\mathsf{D}}$ 

- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

## Application

- Solar inverters
- DC/DC converters
- Switch mode power supplies
- Induction heating
- Motor drives

## • Absolute maximum ratings $(T_a = 25^{\circ}C)$

Parameter	Symbol	Value	Unit	
Drain - Source Voltage	V <sub>DSS</sub>	650	V	
$T_c = 25^{\circ}C$		I <sub>D</sub> <sup>*1</sup>	38	А
Continuous Drain current	$T_c = 100^{\circ}C$	ا <sub>D</sub> *1	27	А
Pulsed Drain current		I <sub>D,pulse</sub> <sup>*2</sup> 95		А
Gate - Source voltage (DC)		V <sub>GSS</sub>	-4 to +22	V
Gate - Source surge voltage (t <sub>surge</sub> < 300ns)		V <sub>GSS_surge</sub> *3	-4 to +26	V
Recommended drive voltage		V <sub>GS_op</sub> <sup>*4</sup>	0 / +18	V
Junction temperature		T <sub>j</sub>	175	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +175	°C

## •Electrical characteristics ( $T_a = 25^{\circ}C$ )

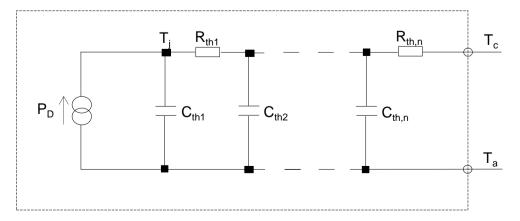
Parameter	Symbol	Conditions		Unit			
Faidilielei	Symbol	Conditions	Min.	Тур.	Max.	Unit	
		$V_{GS} = 0V, I_D = 1mA$					
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	T <sub>j</sub> = 25°C	650	-	-	V	
		T <sub>j</sub> = -55°C	650	-	-		
		$V_{GS} = 0V, V_{DS} = 650V$					
Zero Gate voltage Drain current	I <sub>DSS</sub>	T <sub>j</sub> = 25°C	-	1	10	μA	
		T <sub>j</sub> = 150°C	-	2	-		
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +22V, V_{DS} = 0V$	-	-	100	nA	
Gate - Source leakage current	I <sub>GSS-</sub>	$V_{GS} = -4V$ , $V_{DS} = 0V$	-	-	-100	nA	
Gate threshold voltage	V <sub>GS (th)</sub>	$V_{DS} = 10V, I_{D} = 6.67mA$	2.7	-	5.6	V	
		V <sub>GS</sub> = 18V, I <sub>D</sub> = 13A					
Static Drain - Source on - state resistance	$R_{DS(on)}^{*5}$	$T_j = 25^{\circ}C$	-	60	78	mΩ	
		T <sub>j</sub> = 150°C	-	86	-		
Gate input resistance	R <sub>G</sub>	f = 1MHz, open drain	-	12	-	Ω	

#### •Thermal resistance

Parameter	Symbol	Values			Unit
Falameter		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case <sup>*6</sup>	R <sub>thJC</sub>	-	0.73	0.94	°C/W

## •Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R <sub>th1</sub>	1.14×10 <sup>-1</sup>		C <sub>th1</sub>	2.18×10 <sup>-3</sup>	
R <sub>th2</sub>	1.31×10 <sup>-1</sup>	K/W	C <sub>th2</sub>	3.70×10 <sup>-2</sup>	Ws/K
R <sub>th3</sub>	4.56×10 <sup>-1</sup>		C <sub>th3</sub>	1.09×10 <sup>-2</sup>	





## ●Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Symbol	Conditions	Values			Linit
Parameter	Symbol		Min.	Тур.	Max.	Unit
Transconductance	𝔤 <sub>fs</sub> <sup>∗5</sup>	$V_{DS} = 10V, I_{D} = 13A$	-	4.9	-	S
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0V$	-	852	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 500V	-	55	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	24	-	-
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V$ to 300V	-	126	-	pF
Total Gate charge	$Q_g^{*5}$	V <sub>DS</sub> = 300V I <sub>D</sub> = 13A	-	58	-	
Gate - Source charge	Q <sub>gs</sub> <sup>*5</sup>	$V_{GS} = 18V$	-	11	-	nC
Gate - Drain charge	$Q_{gd}$ *5	See Fig. 1-1.	-	31	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DS} = 400V$ $I_{D} = 13A$	-	5	-	
Rise time	t <sub>r</sub> *5	V <sub>GS</sub> = 0V/+18V	-	14	-	20
Turn - off delay time	t <sub>d(off)</sub> *5	$R_{G} = 0\Omega, L = 750 \mu H$ L <sub>σ</sub> = 50nH, C <sub>σ</sub> = 10pF	-	17	-	ns
Fall time	t <sub>f</sub> *5	See Fig. 2-1, 2-2, 2-3.	-	13	-	
Turn - on switching loss	E <sub>on</sub> *5	E <sub>on</sub> includes diode reverse recovery.	-	79	-	1
Turn - off switching loss	${\sf E}_{\sf off}$ *5		-	18	-	μJ

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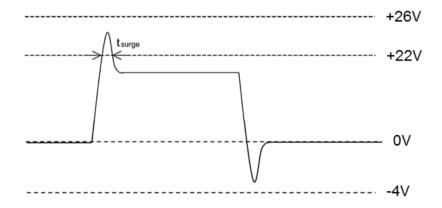
•Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Deremeter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Body diode continuous, forward current	ا <sub>S</sub> *1	T <sub>c</sub> = 25°C	-	-	38	А
Body diode direct current, pulsed	I <sub>SM</sub> *2	T <sub>c</sub> = 25 C	-	-	95	А
Forward voltage	$V_{SD}$ *5	$V_{GS} = 0V, I_D = 13A$	-	3.2	-	V
Reverse recovery time	t <sub>rr</sub> *5	$I_F = 13A$ $V_R = 400V$	-	18	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	di/dt = 2500A/µs	-	294	-	nC
Peak reverse recovery current	<sup>*5</sup>	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	27	-	A

\*1 Limited by maximum temperature allowed.

\*2  $P_W \le 10\mu s$ , Duty cycle  $\le 1\%$ 

\*3 Example of acceptable  $V_{GS}$  waveform



Please note especially when using driver source that  $V_{\text{GSS}\_surge}$  must be in the range of absolute maximum rating.

\*4 Please be advised not to use SiC-MOSFETs with V<sub>GS</sub> below 13V as doing so may cause thermal runaway.

#### \*5 Pulsed

\*6 The case is bottom of leadframe underneath the chip. Practial value of Rth(j-c) is influenced by design of the user. Discribed value is only vaild at the specific conditions such as JESD51-14.

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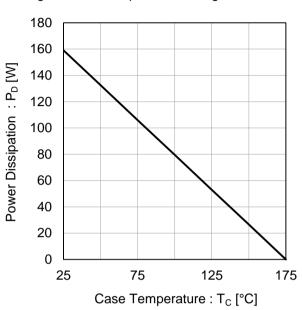
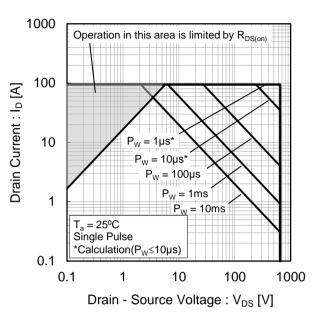
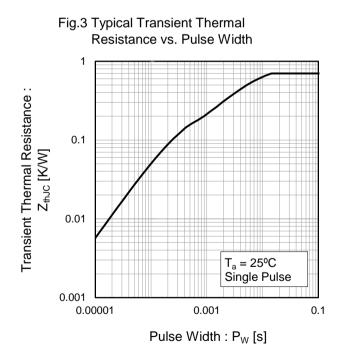


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area







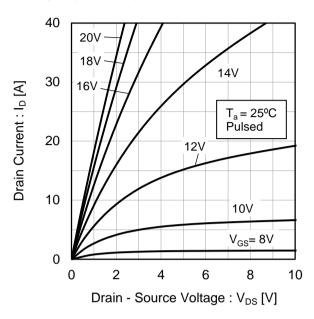
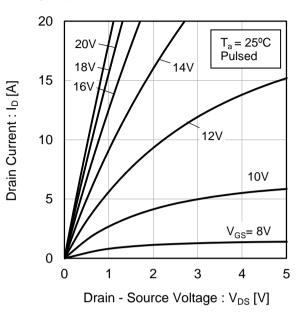
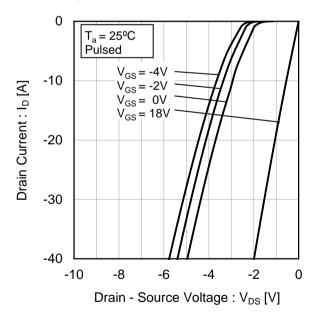


Fig.4 Typical Output Characteristics(I)

Fig.5 Typical Output Characteristics(II)

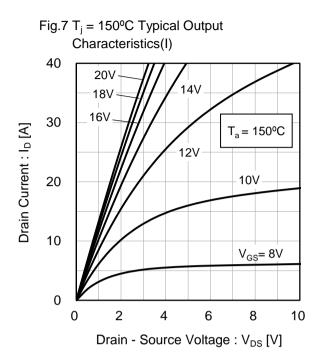


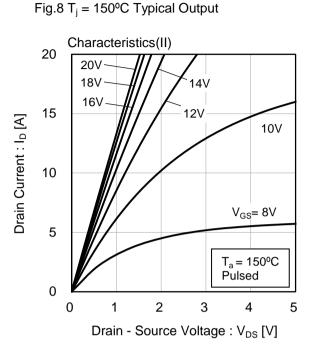
#### Fig.6 $T_j = 25^{\circ}C$ 3rd Quadrant Characteristics



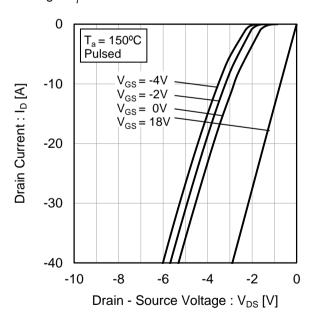


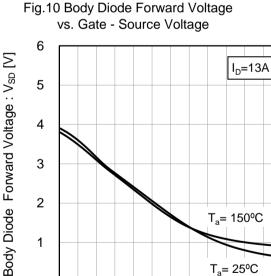


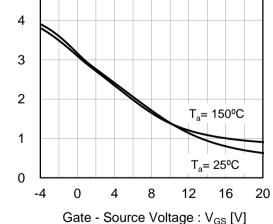




#### Fig.9 T<sub>i</sub> = 150°C 3rd Quadrant Characteristics









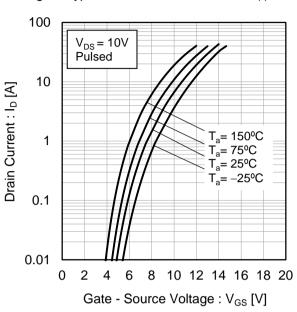
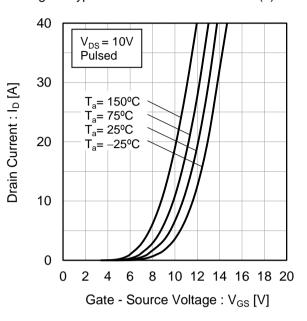


Fig.11 Typical Transfer Characteristics (I)

Fig.12 Typical Transfer Characteristics (II)



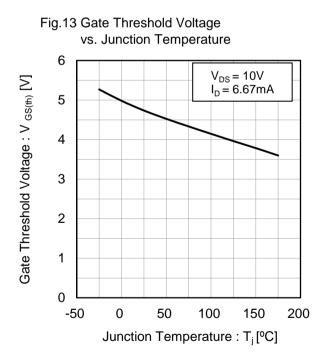
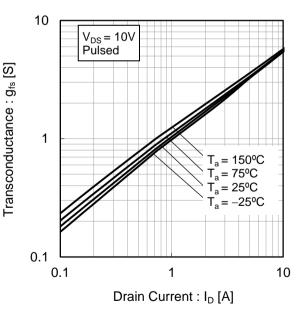
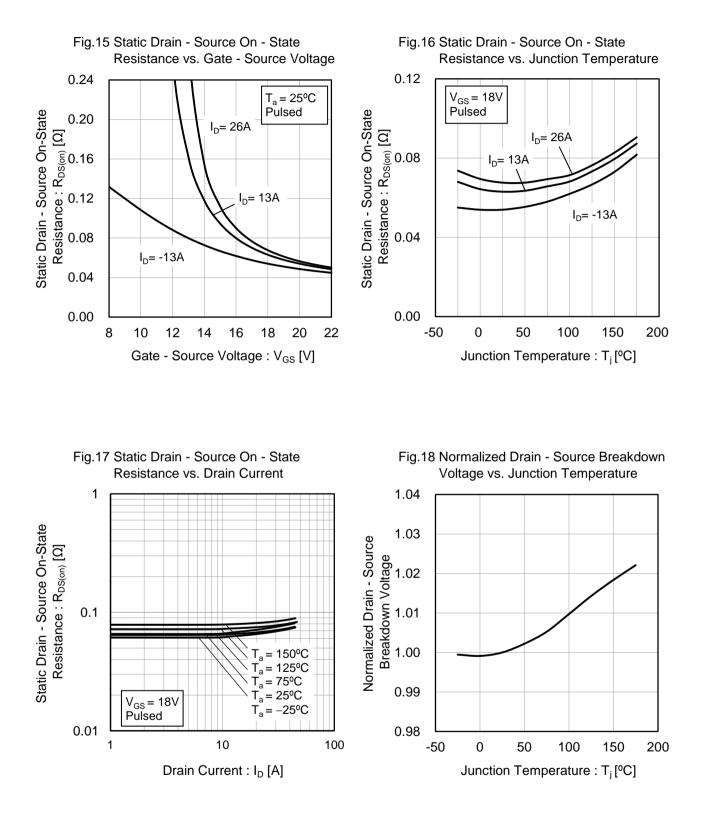
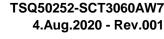


Fig.14 Transconductance vs. Drain Current

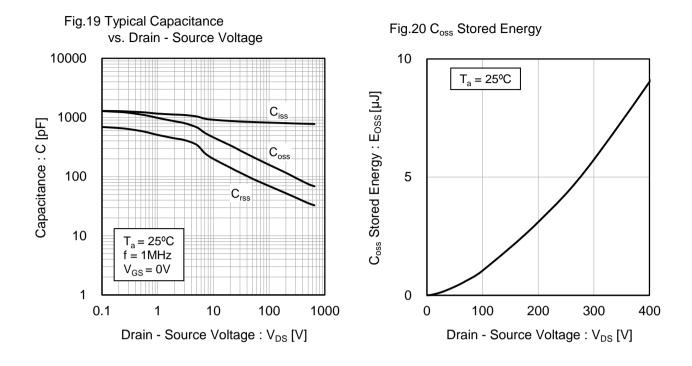




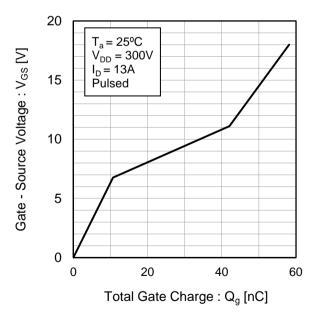


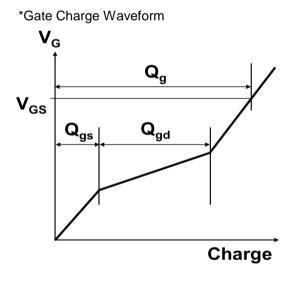


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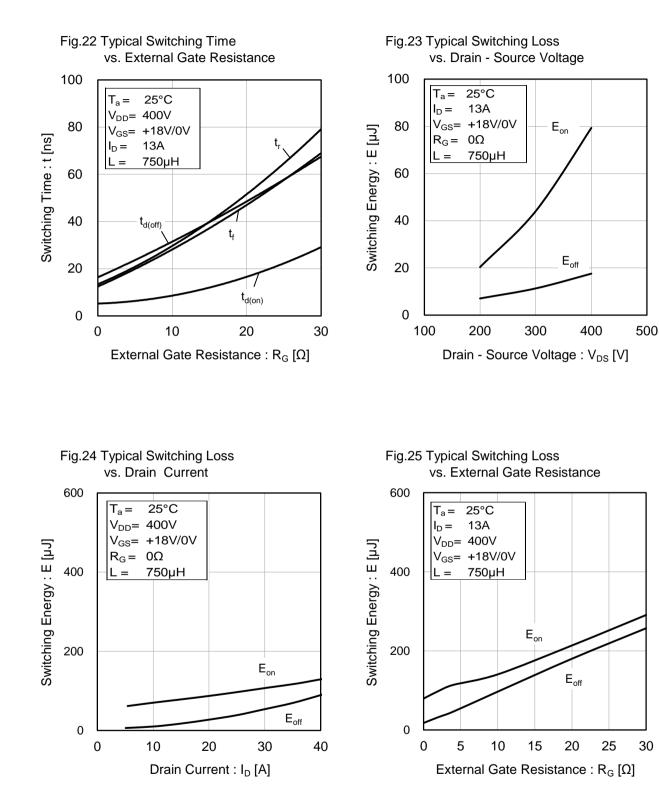


#### Fig.21 Dynamic Input Characteristics













## Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

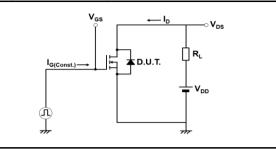


Fig.2-1 Switching Characteristics Measurement Circuit

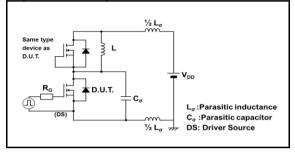


Fig.2-3 Waveforms for Switching Energy Loss

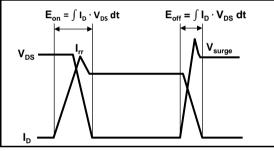
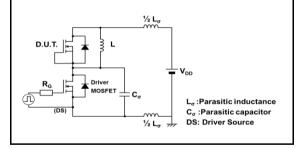


Fig.3-1 Reverse Recovery Time Measurement Circuit



#### Fig.2-2 Waveforms for Switching Time

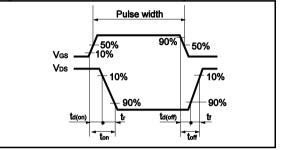
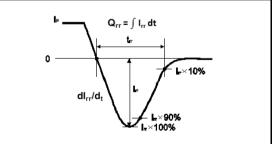


Fig.3-2 Reverse Recovery Waveform







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